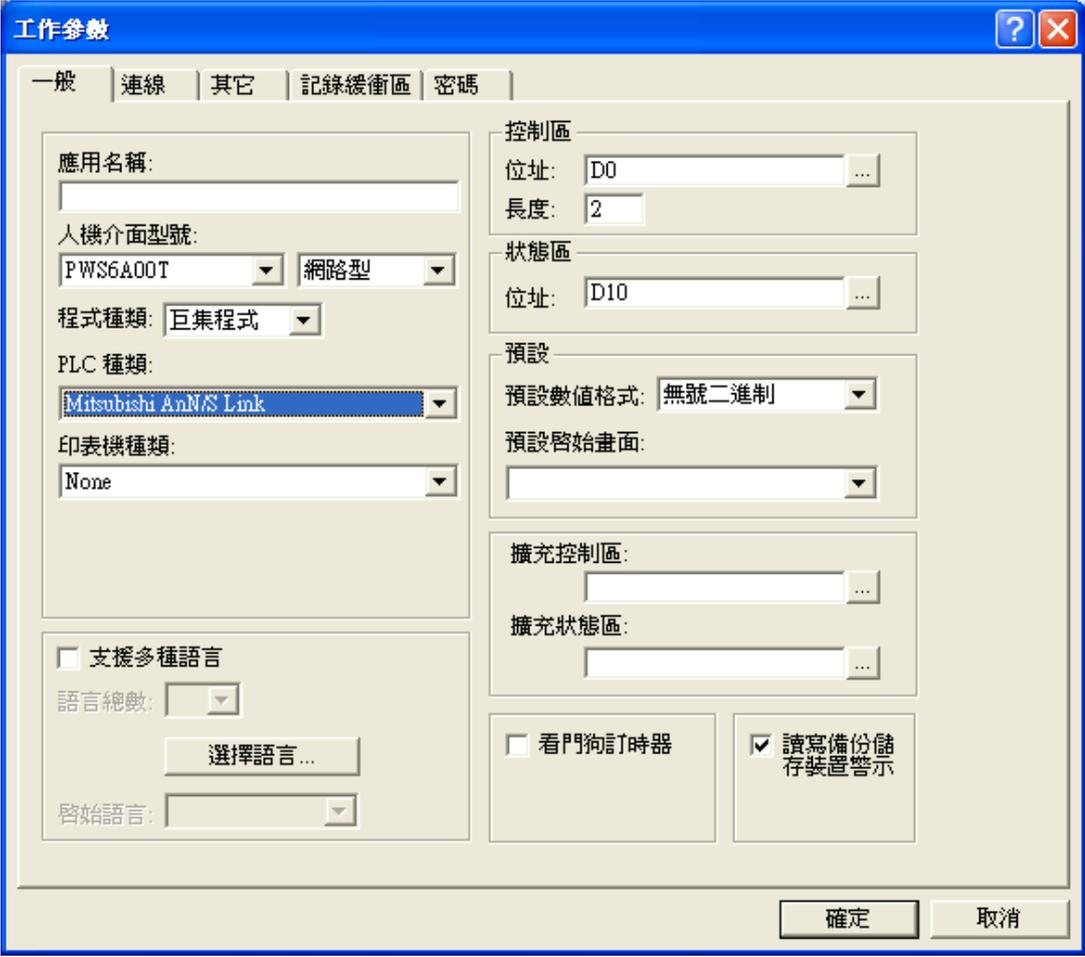
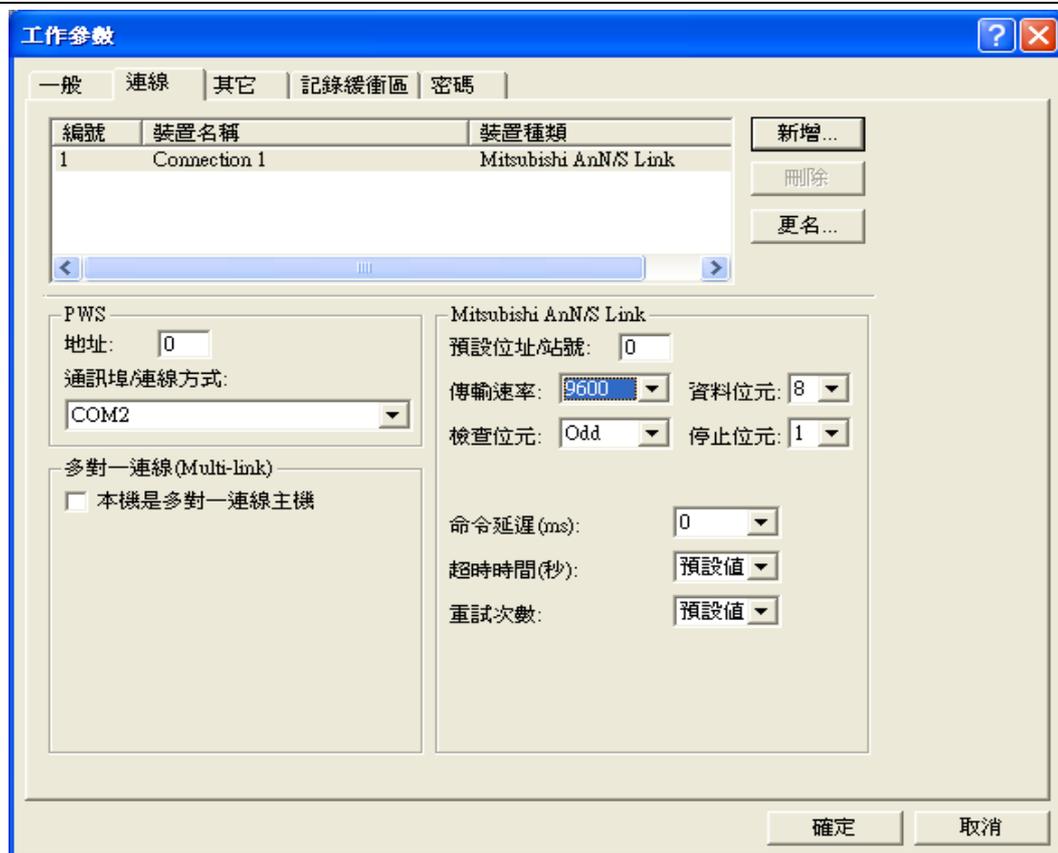


| | | | | | |
|---|---|--------|----------------------|------|--------------|
| 連線名稱 | Mitsubishi AnN/S Link | | | | |
| 人機種類 | PWS6A00-T | PLC 種類 | A1SHCPU + A1SJ71UC24 | 使用版本 | ADP 6.1.1.03 |
| ADP6 軟體設定 | | | | | |
| <ol style="list-style-type: none"> 1. 功能表【應用】中選取【設定工作參數】 | | | | | |
| <ol style="list-style-type: none"> 2. 選擇人機介面型號與 PLC 種類，如下圖： | | | | | |
| 人機設定 |  | | | | |
| <ol style="list-style-type: none"> 3. 點選【連線】，進入設定與 PLC 之通訊協議 (設定的格式需與 PLC 搭配，此例以預設值)，輸入完成後按【確認】。如下圖： | | | | | |



PWS 硬體設定

1. 通訊由 ADP6 軟體決定：

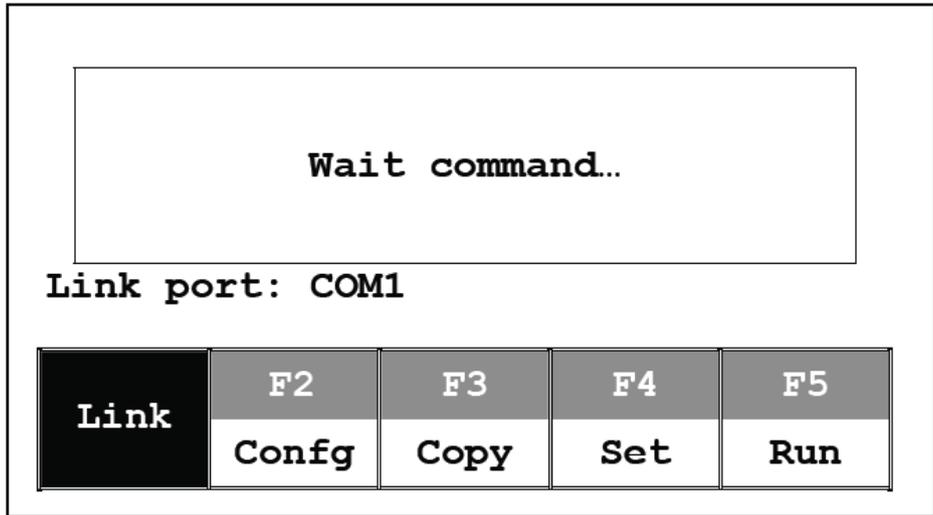
※ 人機背面之指撥開關，SW5、SW6 撥到 OFF 的位置（若使用 R4 模組連接人機的 COM2 則 SW10 需撥到 OFF）。

（有關指撥開關之定義與說明，請參考 ADP6 操作手冊）

2. 通訊由 ADP6 硬體決定：

※ 人機背面之指撥開關，SW6 撥到 OFF，SW5、SW7 撥到 ON 的位置。

（若使用 R4 模組連接人機的 COM2 則 SW10 需撥到 OFF）。當開機並完成自我偵測後，會出現以下畫面。



按下【F2】 Config ，設定人機時間及其通訊參數，使用 ↑↓←→ 鍵移動至欲設定之選項，+ 及 - 鍵改變其值。

PLC 軟體設定

無須設定

PLC 硬體設定

PLC 通訊模組需依下圖表調整適合之通訊設定：

※A1SJ71UC24-R2 :

| Setting of Switches | Setting Switches | Setting Items | Position Setting Switch | |
|---------------------|------------------|----------------------------|-------------------------|--------------|
| | | | ON | OFF |
| | SW03 | Unuse | ----- | ----- |
| | SW04 | Write During RUN | Enabled | Disabled |
| | SW05 | Transmission speed Setting | 300 OFF | 400 ON |
| | SW06 | | 1200 OFF | 2400 ON |
| | SW07 | | 4800 OFF | 9600 ON |
| | | | 19200 OFF | Un-usable ON |
| | SW08 | Data bit setting | 8 bits | 7bits |
| | SW09 | Parity check | Enabled | Disabled |
| | SW10 | Parity setting | Even | Odd |
| | SW11 | Stop bit | 2 bits | 1 bit |
| | SW12 | Sum check | Enabled | Disabled |

PLC
設定

| Mode Setting Switch | Mode Setting Switch Number | Mode Setting |
|---|----------------------------|--------------------------------|
| | | R-232C |
|  | 0 | Unusable |
| | 1 | Type 1 dedicated Protocol mode |
| | 2 | Type 1 dedicated Protocol mode |
| | 3 | Type 1 dedicated Protocol mode |
| | 4 | Type 1 dedicated Protocol mode |
| | 5 | Use prohoibited |
| | 6 | |
| | 7 | |
| | 8 | |
| | 9 | |
| | A | |
| | B | |
| | C | |
| | D | |
| | E | |
| | F | |

※A1SJ71UC24-R4 :

| Setting of Switches | Setting Switches | Setting Items | Position Setting Switch | | | | | | | |
|---------------------|------------------|--|-------------------------|----------------|------|----------|------|------|-------|-----------|
| | | | ON | OFF | | | | | | |
| | SW01 | Unused | ----- | ----- | | | | | | |
| | SW02 | Computer Link/multidrop Link selection | Computer Link | Multidrop link | | | | | | |
| | SW03 | Unused | ----- | ----- | | | | | | |
| | SW04 | Write During RUN | Enabled | Disabled | | | | | | |
| | SW05 | Transmission speed Setting | 300 | 400 | 1200 | 2400 | 4800 | 9600 | 19200 | Un-usable |
| | SW06 | | OFF | ON | OFF | ON | OFF | ON | OFF | ON |
| | SW07 | | OFF | OFF | ON | ON | OFF | OFF | ON | ON |
| | SW08 | Data bit setting | 8 bits | | | 7bits | | | | |
| | SW09 | Parity check | Enabled | | | Disabled | | | | |
| | SW10 | Parity setting | Even | | | Odd | | | | |
| | SW11 | Stop bit | 2 bits | | | 1 bit | | | | |
| | SW12 | Sum check | Enabled | | | Disabled | | | | |

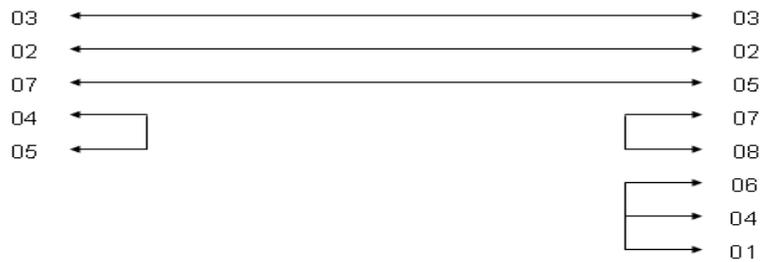
| Mode Setting Switch | Mode Setting Switch Number | Mode Setting |
|---|----------------------------|--------------------------------|
| | | R-422 |
|  | 0 | Use prohibited |
| | 1 | Use prohibited |
| | 2 | |
| | 3 | |
| | 4 | Non procedure mode |
| | 5 | Type 1 dedicated Protocol mode |
| | 6 | Type 2 dedicated Protocol mode |
| | 7 | Type 3 dedicated Protocol mode |
| | 8 | Type 4 dedicated Protocol mode |
| | 9 | Use prohibited |
| | A | |
| | B | |
| | C | |
| | D | Use prohibited |
| | E | |
| | F | |
| F | For module | |

PWS 人機 對 A-LINK(R2)

(25PIN D型公接頭)



(9PIN D型母接頭)

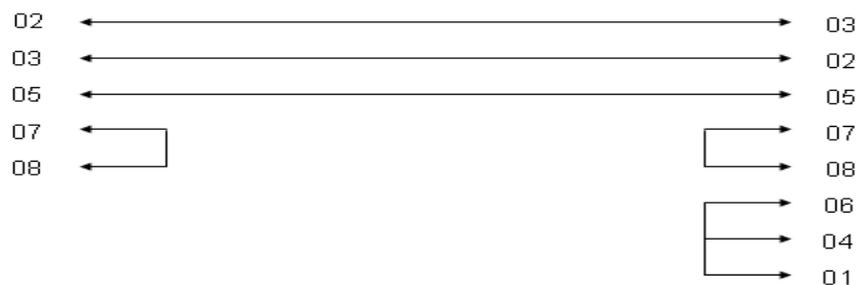


PWS 人機 對 A-LINK(R2)

(9PIN D型公接頭)



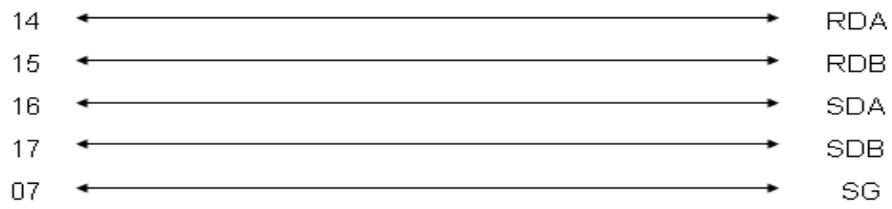
(9PIN D型母接頭)



接
線
圖

PWS 人機 對 A-LINK(R4)

(25PIN D型公接頭)



備註

